

|   | Type | Hits | Search Text  | DBs   | Time Stamp          |
|---|------|------|--|---|---------------------|
| 1 | IS&R | 4559 | (361/679-681,683,684,686).CCLS   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2003/06/26<br>12:50 |
| 2 | BRS  | 2514 | ((361/679-681,683,684,686).CCLS.) and display                            | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2003/06/26<br>12:50 |
| 3 | BRS  | 845  | ((((361/679-681,683,684,686).CCLS.) and display) and processor           | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2003/06/26<br>12:51 |
| 4 | BRS  | 2    | (((((361/679-681,683,684,686).CCLS.) and display) and processor) and str | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2003/06/26<br>13:02 |
| 5 | IS&R | 2    | ("5825617").PN.  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2003/06/26<br>13:10 |
| 6 | BRS  | 32   | suspend-to-ram   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2003/06/26<br>13:39 |
| 7 | BRS  | 32   | "suspend-to-ram"   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2003/06/26<br>13:11 |
| 8 | BRS  | 18   | "suspend-to-ram" and str   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2003/06/26<br>13:11 |
| 9 | BRS  | 16   | ("suspend-to-ram" and processor) and display                             | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2003/06/26<br>13:37 |

09/752,049

|    | Type | Hits | Search Text   | DBs   | Time Stamp          |
|----|------|------|---|---|---------------------|
| 10 | IS&R | 982  | (361/686).CCLS.   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2003/06/26<br>13:37 |
| 11 | BRS  | 262  | ((361/686).CCLS.) and docking   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2003/06/26<br>13:37 |
| 12 | BRS  | 232  | (((361/686).CCLS.) and docking)<br>and (display or monitor or screen<br>or panel)                         | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2003/06/26<br>13:39 |
| 13 | BRS  | 0    | suspend-to-ram and<br>((((361/686).CCLS.) and docking)<br>and (display or monitor or screen<br>or panel)) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2003/06/26<br>13:42 |
| 14 | BRS  | 89   | (((361/686).CCLS.) and docking)<br>and (display or monitor or screen<br>or panel)) and processor          | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2003/06/26<br>13:42 |
| 15 | BRS  | 24   | "suspend-to-ram" and processor  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2003/06/27<br>14:46 |